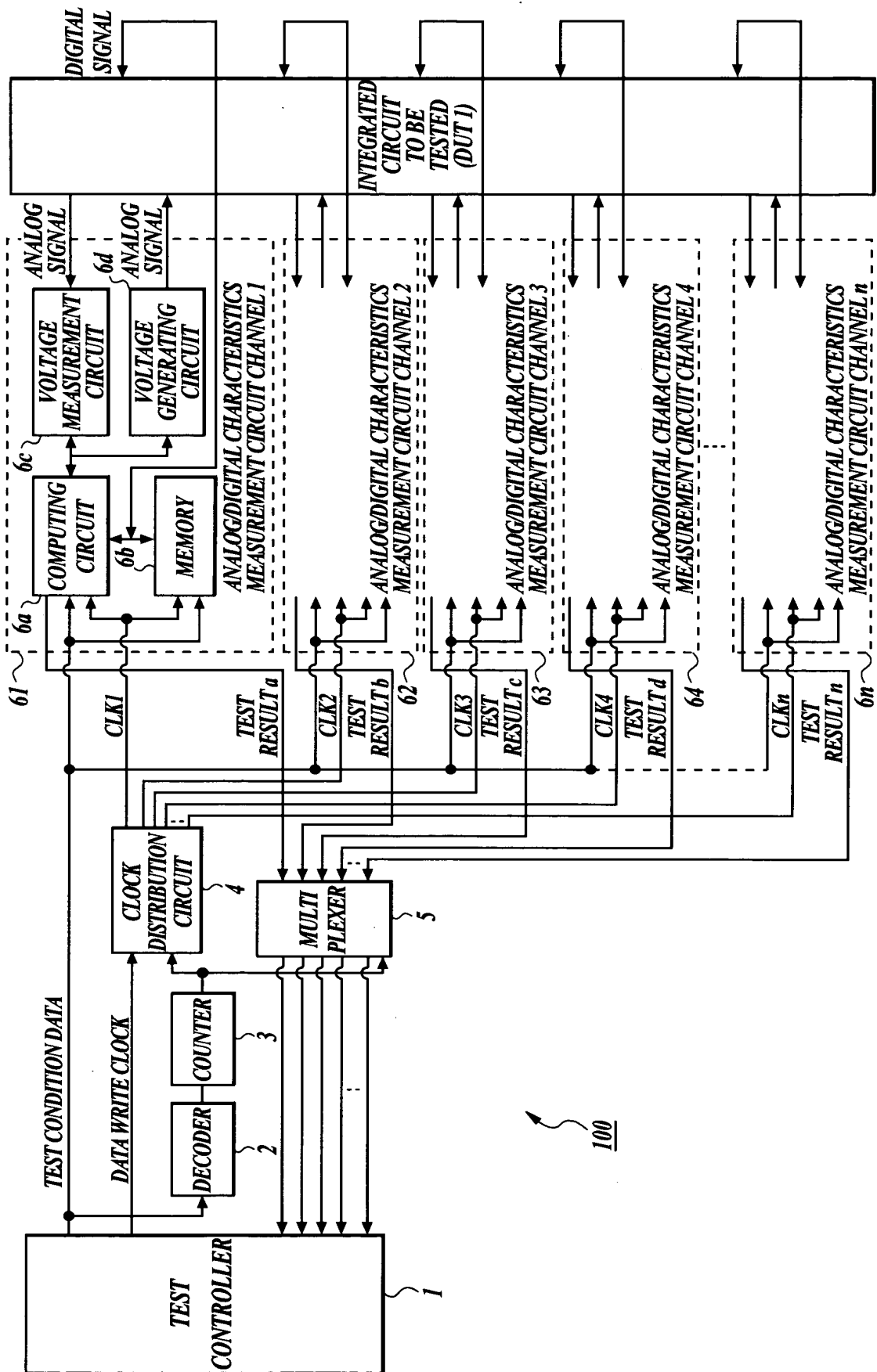


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FIG. 1



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**FIG.2**

**DUT TEST PROGRAM**

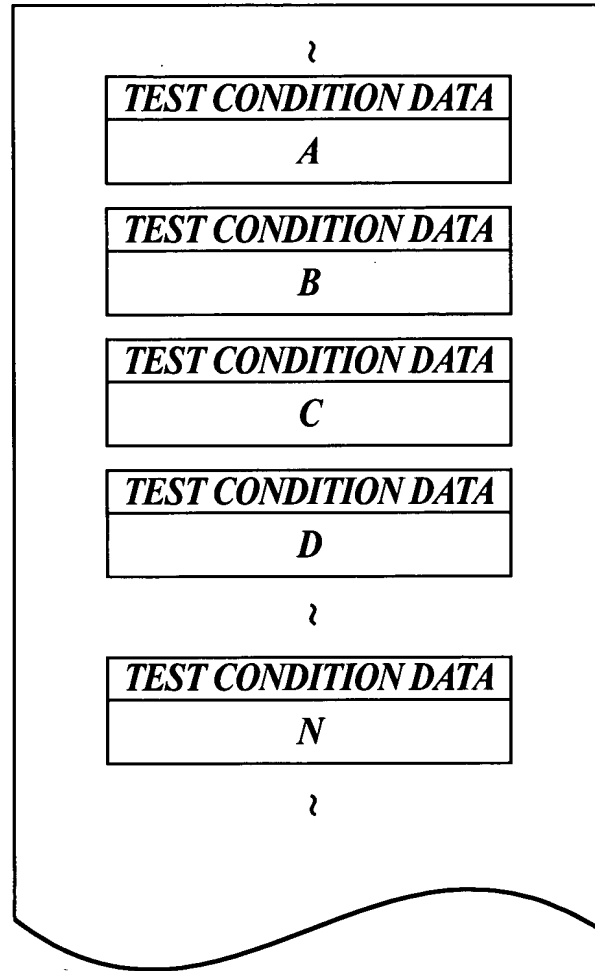
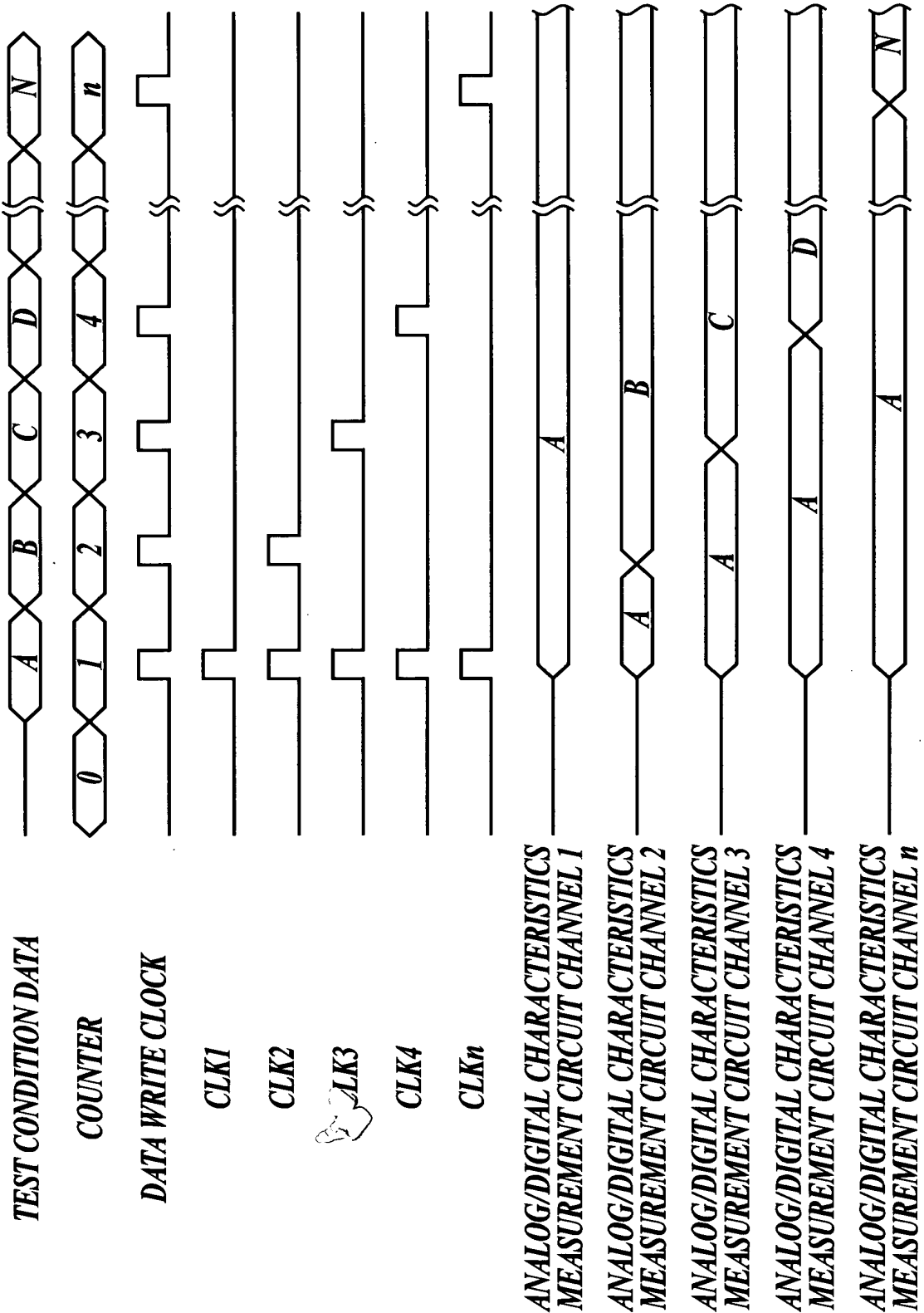


FIG. 2 FOR THE DEVICE

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**FIG.3**

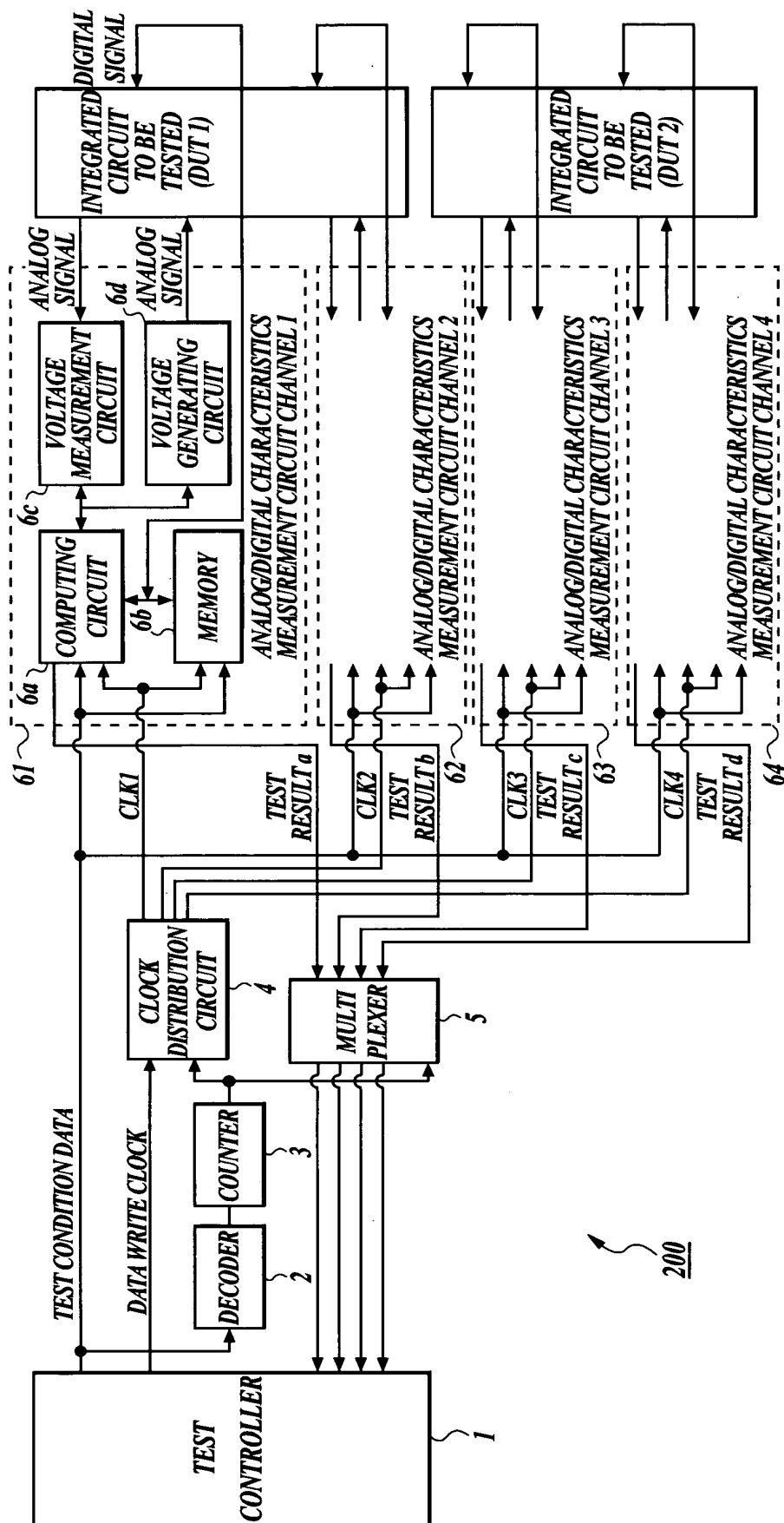


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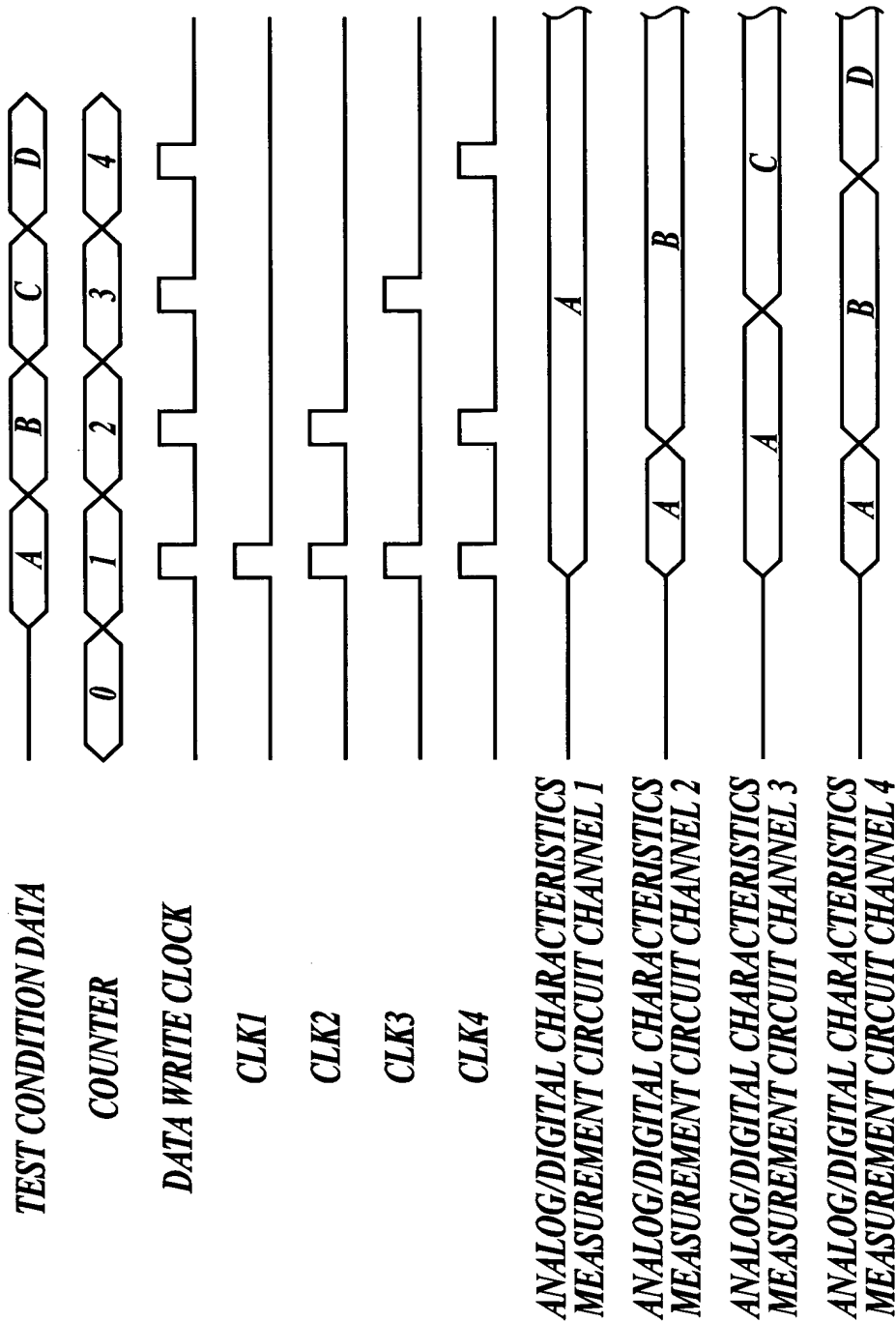
FIG. 4

COUNTER VALUE	IN1	IN2	IN3	IN4	INn	OUT1	OUT2	OUT3	OUT4	OUTn
1	TEST RESULT a	TEST RESULT b	TEST RESULT c	TEST RESULT d	TEST RESULT n	TEST RESULT a	TEST RESULT b	TEST RESULT c	TEST RESULT d	TEST RESULT n
2	TEST RESULT a	TEST RESULT b	-	-	-	TEST RESULT a TEST RESULT b	-	-	-	-
3	TEST RESULT a	TEST RESULT b	TEST RESULT c	-	-	TEST RESULT a TEST RESULT b TEST RESULT c	-	-	-	-
4	TEST RESULT a	TEST RESULT b	TEST RESULT c	TEST RESULT d	-	TEST RESULT a TEST RESULT b TEST RESULT c TEST RESULT d	-	-	-	-
~	~	~	~	~	~	~	~	~	~	~
n	TEST RESULT a	TEST RESULT b	TEST RESULT c	TEST RESULT d	TEST RESULT n	TEST RESULT a TEST RESULT b TEST RESULT c TEST RESULT d ~ TEST RESULT n	~	~	~	~

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**FIG. 5**



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**FIG.6**



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**FIG. 7**

COUNTER VALUE	IN1	IN2	IN3	IN4	OUT1	OUT2	OUT3	OUT4
1	TEST RESULT a	TEST RESULT b	TEST RESULT c	TEST RESULT d	TEST RESULT a	TEST RESULT b	TEST RESULT c	TEST RESULT d
2	TEST RESULT a	TEST RESULT b	TEST RESULT c	TEST RESULT d	TEST RESULT a TEST RESULT b	TEST RESULT c TEST RESULT d	-	-
3	TEST RESULT a	TEST RESULT b	TEST RESULT c	TEST RESULT d	TEST RESULT a TEST RESULT b TEST RESULT c	-	-	-
4	TEST RESULT a	TEST RESULT b	TEST RESULT c	TEST RESULT d	TEST RESULT a TEST RESULT b TEST RESULT c TEST RESULT d	-	-	-

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**FIG. 8**

